

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"6188312".pn.	USPAT; EPO	OR	OFF	2007/02/12 10:15
S2	315	shibazaki.in.	USPAT; EPO	OR	OFF	2007/02/12 10:20
S3	5	"541040".ap.	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/14 14:40
S4	172	gat\$4 near2 clock\$1 with interrupt\$4	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 11:21
S5	19	gat\$4 near2 clock\$1 with interrupt\$4 and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 12:55
S6	102	gat\$4 near2 clock\$1 with idl\$4	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 12:56
S7	37	gat\$4 near2 clock\$1 with idl\$4 and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 13:00
S8	7	gat\$4 near2 clock\$1 with idl\$4 same ("AND gate" "AND gates")	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:49
S9	8	connolly-mark.xa. and gat\$4 near2 clock\$1	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:14
S10	766	cycle adj steal\$4	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:14
S11	98	cycle adj steal\$4 with clock\$1	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:15
S12	12	cycle adj steal\$4 with clock\$1 and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:15
S13	1	"541040".ap. and interrupt adj factor\$1 near5 microprocessor	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:43
S14	1	"541040".ap. and interrupt adj factor\$1 near5 clock adj control	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:43

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S15	15	gat\$4 near2.clock\$1 with idl\$4 same counter\$1	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:51
S16	171	clock\$1 near6 counter\$1 with idl\$4	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 14:51
S17	28	clock\$1 near6 counter\$1 with idl\$4 and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/12 15:29
S30	5	"541040".ap.	US-PGPUB ; USPAT; EPO	OR	OFF	2007/02/13 09:55
S31	0	interrupt near5 S30 and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 08:48
S32	7	interrupt near5 "S3" and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 08:49
S33	1289	713/5\$,6\$.ccls. and counter\$1	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 08:50
S34	433	713/5\$,6\$.ccls. and counter\$1 with pulse\$1	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 08:50
S35	346	713/5\$,6\$.ccls. and counter\$1 near6 pulse\$1	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 08:56
S36	78	713/5\$,6\$.ccls. and counter\$1 near6 pulse\$1 with gat\$4	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 08:58
S37	3	713/5\$,6\$.ccls. and 713/3\$. ccls.and counter\$1 near6 pulse\$1 with gat\$4	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 08:58
S38	3	713/5\$,6\$.ccls. and 713/3\$. ccls. and counter\$1 near6 pulse\$1 with gat\$4	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 08:58
S39	23	713/5\$,6\$.ccls. and 713/3\$. ccls. and counter\$1 near6 pulse\$1	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 09:10
S40	41	713/322.ccls. and counter\$1 near6 pulse\$1	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 09:18

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S41	14	713/322.ccls. and pulse\$1 adj train	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 09:24
S42	40	713/322.ccls. and gat\$4 adj3 pulse\$1	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 09:25
S43	2	hanaki-hirokazu.in.	USPAT; EPO	OR	OFF	2007/02/13 11:39
S44	3	hanaki-hirokazu.in.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 11:46
S45	6251	(enabl\$4 disabl\$4) near5 clock near2 puls\$4	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 11:46
S46	214	(enabl\$4 disabl\$4) near5 clock near2 puls\$4 and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 11:46
S47	0	(enabl\$4 disabl\$4) near5 clock near2 puls\$4 same 713/3\$.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 11:47
S48	24	(enabl\$4 disabl\$4) near5 clock near2 puls\$4 and 713/322\$.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 12:04
S49	165	(divid\$4 near3 clock\$1) near5 counter\$1 and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 12:05
S50	19	(divid\$4 near3 clock\$1) near5 counter\$1 same idl\$4	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 12:17
S51	66	(processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4) with throttl\$4	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 14:56
S52	0	throttl\$4 near7 (interrupt near2 type\$1)	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 13:15
S53	2877	(interrupt near2 type\$1)	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 13:16
S54	0	(interrupt near2 type\$1) and hanaki.in.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/13 13:16

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S55	5	(under\$1voltage\$1 brownout) with throttl\$4	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 12:34
S56	899	(processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4) and "713"/\$.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 14:56
S57	1602	(processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4 stand\$4by operati\$3) and "713"/32\$. ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 14:59
S58	545	(processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4 stand\$4by operati\$3) and "713"/322. ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 14:59
S59	376	(processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4 stand\$4by operati\$3) with (clock\$4 frequenc\$4) and "713"/322. ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 15:00
S60	324	((processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4 stand\$4by operati\$3)) near5 (clock\$4 frequenc\$4) and "713"/322. ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 15:15
S61	28	((processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4 stand\$4by operati\$3)) near5 (clock\$4 frequenc\$4) same counter\$1 and "713"/322.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 15:21
S62	0	((processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4 stand\$4by operati\$3)) near5 bus same (clock\$4 frequenc\$4) same counter\$1 and "713"/322. ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 15:21

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S63	44	((processor\$1 cpu micro\$1processor\$1) near3 (busy idl\$4 stand\$4by operati\$3)) near5 bus same (clock\$4 frequenc\$4) and "713"/322.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/14 15:21
S64	290	interrupt\$3 near4 (processor\$1 cpu micro\$1processor\$1) and 713/322.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/15 08:56
S65	60	(send\$4 receiv\$4) near3 interrupt\$3 near4 (processor\$1 cpu micro\$1processor\$1) and 713/322.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/15 09:02
S66	4	(thrott\$4) near6 interrupt\$3 near4 (processor\$1 cpu micro\$1processor\$1) and 713/322.ccls.	US-PGPUB ; USPAT; EPO; JPO	OR	OFF	2007/02/15 09:02